

Vacuum-deposited organic thin-film transistors based on dinaphthotetraphienoacenes

Federico Modesti¹, T. Musiol¹, R. Jouclas², M. Volpi², G. Schweicher², Y. Geerts^{2,3}, P. Erk¹

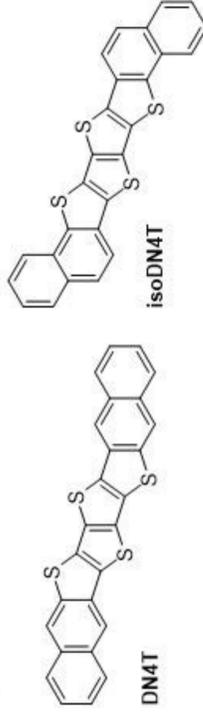
¹ BASF SE, RCS - J542S, 67056 Ludwigshafen am Rhein, Germany

² Laboratoire de Chimie des Polymères, Faculté des Sciences, Université Libre de Bruxelles (ULB), Boulevard du Triomphe, CP 206/01, 1050 Bruxelles, Belgium

³ International Solvay Institutes for Physics and Chemistry, Université Libre de Bruxelles (ULB), Boulevard du Triomphe, CP 231, 1050 Bruxelles, Belgium

Introduction

In the last decades an interest for organic semiconductor films has been growing, due to their application in optical and electrical devices. Here we investigate the electrical performance of DN4T and isoDN4T, two large thienoacenes molecules, by fabricating thin-film field effect transistors (TFTs) through vacuum-sublimation. The structure-properties relationships of the two molecules are investigated using atomic force microscopy, X-ray diffraction along with charge-transport measurements, providing a detailed comparison of the semiconductor thin-film morphology, crystallinity and electrical performances as a function of the substrate temperature. The study contributes to a wider understanding of the relationship between the molecular structure and the charge transport with respect to the thin-film's crystal packing of large thienoacenes.



Materials and methods

TFTs were fabricated through thermal evaporation in high vacuum on highly doped silicon wafer substrates overgrown by atomic layer deposition with a 30 nm thick dielectric layer of Al₂O₃ which was treated with n-tetradecylphosphonic acid (TDPA) prior to the deposition of the semiconductors. Bottom-gate top-contact (BGTC) configuration was fabricated by thermal evaporation of gold with a typical thickness of ca. 50 nm using shadow masks. During the deposition, the substrates were kept at temperatures ranging from 40 to 140°C with 20°C steps, respectively. The evaporation rate of 0.5 Å/s resulted typically film thicknesses of ca. 25 nm. The TFTs have a channel length (L) of 215 μm and a channel width (W) of 480 μm.

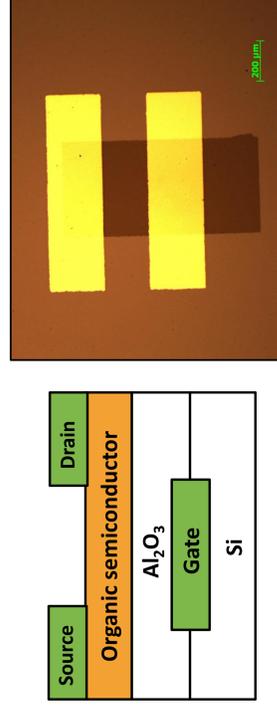
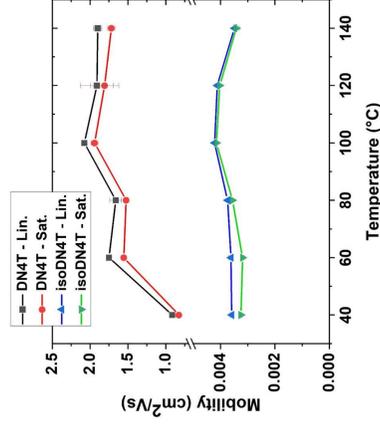
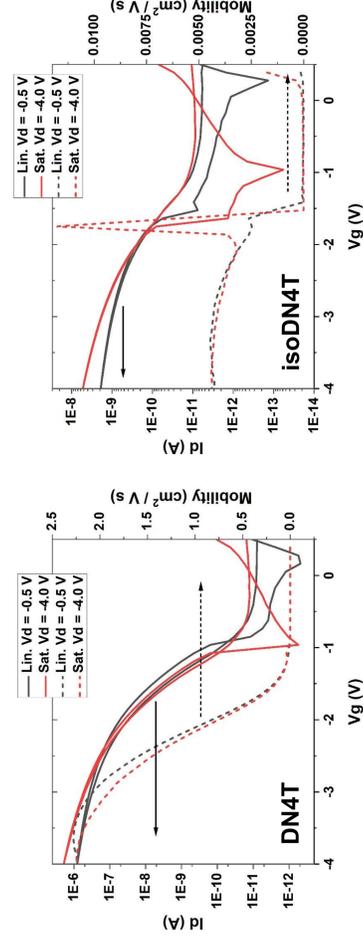


Illustration and optical microscope image of BGTC transistor

Results

❖ Electrical performances

DN4T based devices show a charge carrier mobility of 0.9 up to 2.1 cm²/Vs for DN4T, depending on the substrate temperature along with an on/off current ratios (I_{ON}/I_{OFF}) of ~10⁵-10⁶, while isoDN4T devices gave rather poor performance with a mobility of 0.0029 up to 0.0042 cm²/Vs and I_{ON}/I_{OFF} of ~10²-10³. The TFTs exhibit a threshold voltage between -1.8/ and -2.4 V and for both the isomers the highest charge carrier mobilities were found for devices deposited at a substrate temperature of 100°C.

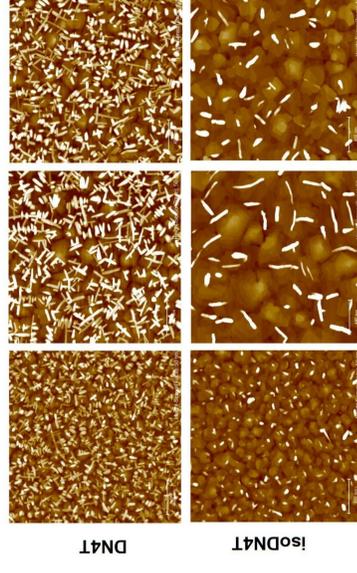


Transfer characteristics of DN4T and isoDN4T at substrate temperature of 100°C

❖ Thin-film morphology and crystallinity

A terrace pattern covered by needle-shaped crystals arises from the images at all substrate temperatures. Particularly for DN4T, the quantity of these three-dimensional needle-shaped crystals is higher compared to isoDN4T samples. Remarkably:

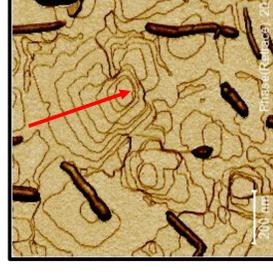
- the longest grain diameter is achieved at a substrate temperature of 100 °C for both DN4T and isoDN4T samples
- the height of the terrace steps corresponds to the length of DN4T and isoDN4T, suggesting that in the crystalline films the molecules are oriented approximately upright with respect to the substrate. This observation is confirmed by XRD patterns.



Mobility as a function of the substrate temperature for DN4T and isoDN4T TFTs

Compound	Substrate T (°C)	Terrace height (Å)	Averaged d-spacing (Å)	Grain diameter (nm)
DN4T	40	20 ± 2	20.2 ± 0.5	145 ± 30
	100	20 ± 1	20.2 ± 0.5	250 ± 60
	140	18 ± 2	20.0 ± 0.5	200 ± 15
isoDN4T	40	18 ± 1	19.2 ± 0.1	190 ± 30
	100	18 ± 2	19.2 ± 0.1	460 ± 75
	140	20 ± 2	20.0 ± 0.5	320 ± 55

Extracted data from AFM and XRD characterization



Top on the left: AFM topography images of DN4T and isoDN4T at different substrate temperature (size of the images is 2x2 μm)
Top on the right: XRD patterns of DN4T and isoDN4T deposited at 100 °C.
Bottom on the left: enlargement of AFM material contrast image, which show the film terrace structure (size of the image is 1 x 1 μm)

Conclusions

- TFTs were fabricated using DN4T and isoDN4T through vacuum-sublimation
- The electrical performance are consistent with the already reported values for DN4T and isoDN4T, confirming the analogy between the two system of thienoacenes
- isoDN4T gave rather poor performance compared to DN4T, confirming the structure-properties relationship and the computational calculations
- The thin-films were characterized through AFM and XRD, elucidating the crystalline arrangement

Acknowledgement



This project has received funding from the European Union's Horizon 2020 research and innovation programme under the Marie Skłodowska-Curie grant agreement No 81128424

References:

1. Yamamoto, T., *J. Am. Chem. Soc.* **129**, 2224–2225 (2007)
2. Yamamoto, T., *BCSJ* **83**, 120–130 (2010)